

Electronic Acknowledgement Receipt

EFS ID:	1056384		
Application Number:	10070092		
Confirmation Number:	7323		
Title of Invention:	Local register instruction for selectively loading a register in a multithreaded parallel processor architecture		
First Named Inventor:	Gilbert Wolrich		
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Filer:	Ido Rabinovitch/Vasilia Kelly		
Filer Authorized By:	Ido Rabinovitch		
Attorney Docket Number:	10559-309US1		
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Application Type:	U.S. National Stage under 35 USC 371		
International Application Number:			

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Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		10559309US1Response.pdf	110012	yes	11
Multipart Description					
Doc Desc		Start		End	
Amendment After Final		1		1	
Amendment Copy Claims/Response to Suggested Claims		2		6	
Applicant Arguments/Remarks Made in an Amendment		7		11	

Warnings:**Information:**

Total Files Size (in bytes):	110012
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.